McIDAS
HOST TO TERMINAL-TERMINAL TO HOST
SYSTEM PROTOCOL DESCRIPTION
11 September 1984

Rev. level: 2
# TABLE OF CONTENTS

I. Introduction ................................................. 1

II. Character Definitions ....................................... 1

III. Communication Modes ....................................... 2

IV. Message Form ................................................. 4

V. Routing Codes – Host to Terminal .......................... 5

VI. Routing Codes – Terminal to Host ......................... 21

Appendix A – Routing Code Summary .......................... 29

Index .................................................................. 31
MCIDAS HOST TO TERMINAL—TERMINAL TO HOST SYSTEM PROTOCOL DESCRIPTION
20 July 1983

OUTLINE

I. Introduction

II. General Character Definitions
   A. Control Character Definitions
   B. Control Character Exceptions

III. Communication Modes

IV. General Message Form

V. Routing Codes — Host to Terminal
   A. 4OH, 41H Asymmetric One Bit Protocol IDS (Transactional)
   B. 42H to 8OH Future Expansion
   C. 81H CRT Data — Intensive and Transactional
   D. 82H Terminal Printer — Intensive and Transactional
      1. Graphics erase command
      2. Graphics line segment command
   F. 84H TV Image Line Load 3 Bit Mode — Intensive and Transactional
   G. 85H 6 Bit Enhancement Table Load — Intensive and Transactional
   H. 86H Video Control 6 Bit Mode
   I. 87H TV Image Line Load 6 Bit Mode — Intensive and Transactional
   J. 88H TV Frame to Load — Intensive and Transactional
   K. 89H 12 Bit Enhancement Table Load — Intensive and Transactional
   L. 8AH ID Request — Intensive and Transactional
   M. 8BH Graphics Device (Delta Mode) — Intensive and Transactional
      1. Graphics erase command
      2. Graphics line segment command
   N. 8CH McIVAM Pallet Graphics Colors — Intensive Mode Only
   O. 8DH McIVAM Eigen Disk Record — Intensive Mode Only
   P. 8EH Graphics Fill Mode — Intensive and Transactional
   Q. 8FH Graphics End Point Mode — Intensive and Transactional
   R. 90H Graphics in Both Delta and End Point Mode — Wide Line
      Graphics — Dotted Line Graphics — Intensive and Transactional
   S. 91H TV Image Control — Transactional Mode Only
   T. 92H Graphic Image Control — Transactional Mode Only
   U. 93H Primary Cursor State — Transactional Mode Only
   V. 94H Movie Loop Dwell — Transactional Mode Only
   W. 95H Graphics—Cursor Color Pallet — Intensive and Transactional
   X. 96H Multi-Video Channel Enable—Disable — Transactional Mode
   Y. 97H Terminal State Request — Transactional Mode Only
   Z. 99H Transactional Mode Switch — Intensive and Transactional
   Z1. 9CH Auto Prompt — Transactional Mode Only
   Z2. 9DH Velocity Increment — Transactional Mode Only
   Z3. 9FH Windco Mode State — Transactional Mode Only
   Z4. A0H Stereo Cursor State — Transactional Mode Only
VI. Routing Codes – Terminal to Host
A. Negative Acknowledge (NAK) – Intensive and Transactional
B. Terminal State Response – Intensive Mode Only
C. 40H, 41H Asymmetric One Bit Protocol IDS
D. 81H Keyboard Characters – Transactional Mode Only
E. 8AH ID Response – Transactional Mode Only
F. 91H TV Image State – Transactional Mode Only
G. 92H Graphic Image State – Transactional Mode Only
H. 93H Primary Cursor State – Transactional Mode Only
I. 96H Multi-Video Channel State – Transactional Mode Only
J. 98H Buffer Available Status – Transactional Mode Only
K. 99H Idle Traffic – Transactional Mode Only
L. 9AH Raw Joystick Data – Transactional Mode Only
M. 9BH Data Tablet Data – Transactional Mode Only
N. 9EH Data Tablet Pen State – Transactional Mode Only
O. 9FH Windco Response – Transactional Mode Only
P. A0H Stereo Cursor State – Transactional Mode Only
I. INTRODUCTION

All communications between the host CPU and the McIDAS terminal CPU are accomplished via message packets of a defined structure called a message protocol. This is a description of that protocol. Not all hardware configurations employ all aspects of this definition.

II. GENERAL CHARACTER DEFINITIONS

Communications between the host CPU and the McIDAS terminal are half duplex, byte oriented, binary synchronous, and employ a subset of the IBM bisync protocol. The following are the general classes of bytes:

1) All control characters are in the range of 00H to 3FH (H designates hexadecimal numeric notation)

2) All logical device routing characters are in the range of 40H to FFH

3) All data characters are in the range of 40H to FFH

A. Control Character Definitions

<table>
<thead>
<tr>
<th>CHAR</th>
<th>NAME</th>
<th>DEFINITION</th>
</tr>
</thead>
<tbody>
<tr>
<td>02H</td>
<td>STX</td>
<td>Indicates the next byte is beginning of message data</td>
</tr>
<tr>
<td>03H</td>
<td>ETX</td>
<td>End of message, checksum follows.</td>
</tr>
<tr>
<td>1DH</td>
<td>PENUP</td>
<td>Pen lift control for graphics message packets (routing codes 8BH &amp; 8FH)</td>
</tr>
<tr>
<td>1EH</td>
<td>IRS</td>
<td>Internal record separator, defines end of data for current logical device</td>
</tr>
<tr>
<td>26H</td>
<td>ETB</td>
<td>End of transmitted block, defines end of message, checksum follows</td>
</tr>
<tr>
<td>2DH</td>
<td>ENQ</td>
<td>Used during initial logon for IBM hardware environments</td>
</tr>
<tr>
<td>30H</td>
<td>MODSW</td>
<td>Mode switch control for graphics routing code 90H</td>
</tr>
<tr>
<td>31H</td>
<td>PENUP</td>
<td>Pen lift control for graphics mode switch packets (routing code 90H)</td>
</tr>
<tr>
<td>32H</td>
<td>SYN</td>
<td>Sync pattern used by hardware to frame message</td>
</tr>
</tbody>
</table>
37H  EOT  End of transmission, sent in response to a
        WACK (6BH)
3DH  NAK  Indicates checksum error, retransmission is
        requested

B. Control Character Exceptions (AK0 & AK1)

AK0 is a 2 byte response code.

<table>
<thead>
<tr>
<th>CHAR</th>
<th>NAME</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>10H</td>
<td>AK0</td>
<td>2 byte response to ENQ from IBM hardware environments</td>
</tr>
<tr>
<td>70H</td>
<td>AK0</td>
<td>Used during initial logon, sent by IBM hosts in some bisync modes, terminal response is EOT (37H).</td>
</tr>
</tbody>
</table>

Example:

Host Sends;  SYN,ENQ (i.e. 32H,2DH)
Terminal Responds; SYN,AK0 (i.e. 32H,10H,70H)

AK1 is a 2 byte response code.

<table>
<thead>
<tr>
<th>CHAR</th>
<th>NAME</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>10H</td>
<td>AK1</td>
<td>2 byte response sent by IBM hosts in some bisync modes,</td>
</tr>
<tr>
<td>61H</td>
<td>AK1</td>
<td>terminal response is EOT (37H).</td>
</tr>
<tr>
<td>10H</td>
<td>WACK</td>
<td>Wait acknowledge, sent by IBM hosts.</td>
</tr>
<tr>
<td>6BH</td>
<td>WACK</td>
<td>Indicates wait and last message is acknowledged. Terminal responds with EOT (37H)</td>
</tr>
</tbody>
</table>

III. COMMUNICATION MODES

There are currently two modes of communication:

1) Intensive mode (currently used with Harris CPU hosts)
2) Transactional mode (currently used with IBM and AMDAHL hosts)

During intensive mode, the terminal immediately responds to host messages with its own message. During transactional mode, the terminal responds to the host message at a variable rate that depends upon the nature of the current system activity.
Transactional mode optionally operates under a one-bit asymmetric bisync protocol. Under the protocol, messages between the host and the terminal use the first routing code for message identification. The terminal and the host each switch between two states. Each state expects a message to include a predetermined id as the first routing code. If the correct id is detected, a new message is sent by the receiver, an incorrect id indicates a communication error and the receiver resends the last message.

<table>
<thead>
<tr>
<th>Char</th>
<th>Name</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>40H</td>
<td>IDφ</td>
<td>One bit protocol id routing code</td>
</tr>
<tr>
<td>41H</td>
<td>ID1</td>
<td></td>
</tr>
</tbody>
</table>

The protocol operates under the following rules:

**Terminal state**

| State       | ID=φ                  | ID=1                  | CS ERROR
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>expect ID=φ</td>
<td>accept rcv buf</td>
<td>discard rcv buf</td>
<td>discard rcv buf</td>
</tr>
<tr>
<td></td>
<td>send new xmt buf with ID=φ</td>
<td>resend old xmt buf with ID=1</td>
<td>resend old xmt buf with ID=1</td>
</tr>
<tr>
<td></td>
<td>go to state 1</td>
<td>go to state φ</td>
<td>go to state φ</td>
</tr>
<tr>
<td>state 1</td>
<td>discard rcv buf</td>
<td>accept rcf buf</td>
<td>discard rcv buf</td>
</tr>
<tr>
<td>expect ID=1</td>
<td>resend old xmt buf with ID=φ</td>
<td>send new xmt buf with ID=1</td>
<td>resend old xmt buf with ID=φ</td>
</tr>
<tr>
<td></td>
<td>go to state 1</td>
<td>go to state φ</td>
<td>go to state 1</td>
</tr>
</tbody>
</table>

**Host state**

| State       | ID=φ                  | ID=1                  | CS ERROR
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>expect ID=φ</td>
<td>accept rcv buf</td>
<td>discard rcv buf</td>
<td>discard rcv buf</td>
</tr>
<tr>
<td></td>
<td>send new xmt buf with ID=1</td>
<td>resend old xmt buf with ID=φ</td>
<td>resend old xmt buf with ID=φ</td>
</tr>
<tr>
<td></td>
<td>go to state 1</td>
<td>go to state φ</td>
<td>go to state φ</td>
</tr>
<tr>
<td>state 1</td>
<td>discard rcv buf</td>
<td>accept rcf buf</td>
<td>discard rcv buf</td>
</tr>
<tr>
<td>expect ID=1</td>
<td>resend old xmt buf with ID=1</td>
<td>send new xmt buf with ID=φ</td>
<td>resend old xmt buf with ID=1</td>
</tr>
<tr>
<td></td>
<td>go to state 1</td>
<td>go to state φ</td>
<td>go to state 1</td>
</tr>
</tbody>
</table>

Example:

<table>
<thead>
<tr>
<th>host</th>
<th>SYN</th>
<th>SYN</th>
<th>STX</th>
<th>IDφ</th>
<th>IRS</th>
<th>&lt;RT1&gt;</th>
<th>&lt;DATA&gt;</th>
<th>IRS</th>
<th>...</th>
<th>ETX</th>
<th>P1</th>
<th>P2</th>
</tr>
</thead>
<tbody>
<tr>
<td>terminal</td>
<td>SYN</td>
<td>SYN</td>
<td>STX</td>
<td>IDφ</td>
<td>IRS</td>
<td>...</td>
<td>ETX</td>
<td>P1</td>
<td>P2</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>host</th>
<th>SYN</th>
<th>SYN</th>
<th>STX</th>
<th>ID1</th>
<th>IRS</th>
<th>...</th>
<th>ETX</th>
<th>P1</th>
<th>P2</th>
</tr>
</thead>
<tbody>
<tr>
<td>terminal</td>
<td>SYN</td>
<td>SYN</td>
<td>STX</td>
<td>ID1</td>
<td>IRS</td>
<td>...</td>
<td>ETX</td>
<td>P1</td>
<td>P2</td>
</tr>
</tbody>
</table>
IV. GENERAL MESSAGE FORM - HOST TO TERMINAL AND TERMINAL TO HOST

The general form of a message between the host and the terminal is

SYN SYN STX <RT1> <DATA> IRS <RT2> <DATA> IRS ...
... <RTN> <DATA> ETX P1 P2

where <RTX> is a routing code to or from a logical device and <DATA> is zero or more bytes routed to or from that device. The message body from STX to ETX will always be \leq 768 characters. P1 and P2 are a 16 bit checksum (CRC-16 inverse) for the message.

NOTE: All routing codes (host to terminal) send data to logical devices and not physical devices, with no assumptions about hardware implementation within a terminal.

V. ROUTING CODES - HOST TO TERMINAL

All routing codes are in the range of 40H to FFH. All routing codes are preceded by an STX or an IRS. All routing codes are followed by an IRS or an ETX. Terminals that do not have a particular routing code implemented, will treat that particular code as a no-op.

A. Routing Codes 40H and 41H are Used for Asymmetric One Bit Protocol

See communications modes.

B. Routing Codes 42H to 80H are Reserved for Future Expansion

C. Routing Code 81H CRT Data

Both intensive and transactional modes.

This routing code sends ASCII text to the terminal console. All data must be ASCII bytes OR'd with 80H. Data may be from 1 to 82 bytes long.

Example: This is a CRT message consisting of a line feed, A, carriage return.

02H(1EH) 81H 8AH C1H 1EH(03H)

D. Routing Code 82H Terminal Printer

Both intensive and transactional modes.

All data is ASCII OR'd with 80H. First data character is form control according to FORTRAN conventions, i.e.,
<table>
<thead>
<tr>
<th>CHAR</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>BLANK</td>
<td>single space before printing</td>
</tr>
<tr>
<td>0</td>
<td>double space</td>
</tr>
<tr>
<td>1</td>
<td>top of form</td>
</tr>
<tr>
<td>+</td>
<td>no space before printing</td>
</tr>
</tbody>
</table>

The form control character may be followed by from 0 to 132 characters which define one line for the printer.

Example: This is a message that top of forms the line printer and prints an A followed by a carriage return.

```
02H(1EH) 82H B1H C1H 1EH(03H)
```

**E. Routing Code 83H Single Graphics Device (Delta Mode)**

Both intensive and transactional modes.

This routing code sends data to a terminal with single frame graphics. This function allows erasing the graphics frame or writing a line segment of 1 or more points. The graphics frame origin is at the upper left corner of the image space.

1. **Graphics Erase Command**

   The empty message causes the graphics frame to be erased.

   Example: 02H(1EH) 83H 1EH(03H)

2. **Graphics Line Segment Command**

   Four header bytes are required following the 83H (routing code):

   Byte 1  1  1  L4 L3 L2 L1 L0  Y8
   Byte 2  1  1  Y7 Y6 Y5 Y4  Y3  Y2
   Byte 3  1  1  Y1  Y0  X9  X8  X7  X6
   Byte 4  1  1  X5  X4  X3  X2  X1  X0

   where:

   - L4-L0 define the graphics value to write (0-1FH)
   - Y8-Y0 define the TV line to set the pen down (0-499)
   - X9-X0 define the TV pixel to set the pen down (0-639)

   In all cases *0 is the least significant bit. If a single point is to be written, no additional bytes are required. If a line segment is to be drawn, additional data must be packed (using 2 bytes) as five 3-bit values. These 3-bit values define relative delta moves, each in one of the either cardinal directions.
<table>
<thead>
<tr>
<th>3-bit Data Value</th>
<th>Relative Pen Motion</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>$X$ to $X+1$ ; $Y$ to $Y$</td>
</tr>
<tr>
<td>1</td>
<td>$X$ to $X-1$ ; $Y$ to $Y$</td>
</tr>
<tr>
<td>2</td>
<td>$X$ to $X$ ; $Y$ to $Y+1$</td>
</tr>
<tr>
<td>3</td>
<td>$X$ to $X$ ; $Y$ to $Y-1$</td>
</tr>
<tr>
<td>4</td>
<td>$X$ to $X+1$ ; $Y$ to $Y+1$</td>
</tr>
<tr>
<td>5</td>
<td>$X$ to $X-1$ ; $Y$ to $Y+1$</td>
</tr>
<tr>
<td>6</td>
<td>$X$ to $X+1$ ; $Y$ to $Y-1$</td>
</tr>
<tr>
<td>7</td>
<td>$X$ to $X-1$ ; $Y$ to $Y-1$</td>
</tr>
</tbody>
</table>

Furthermore, these 3-bit values must be packed as 5 data values into two bytes according to the following format:

If the two bytes are made of these bits,

\[
\text{Byte 1} = A_1 A_2 B_1 B_2 B_3 C_1 C_2 C_3 \\
\text{Byte 2} = A_3 A_4 D_1 D_2 D_3 E_1 E_2 E_3
\]

then,

\[
\begin{array}{cccccc}
A_1 & A_2 & A_3 & A_4 & \multicolumn{2}{c}{3-bit Delta Code} \\
0 & 1 & 0 & 1 & 0 & 0 \\
0 & 1 & 1 & 0 & 0 & 1 \\
0 & 1 & 1 & 1 & 1 & 2 \\
1 & 0 & 0 & 1 & 1 & 3 \\
1 & 0 & 1 & 0 & 1 & 4 \\
1 & 0 & 1 & 1 & 0 & 5 \\
1 & 1 & 0 & 1 & 1 & 6 \\
1 & 1 & 1 & 0 & 1 & 7 \\
1 & 1 & 1 & 1 & 1 & \text{Unused}
\end{array}
\]

Bit groups B, C, D, and E are themselves the 3-bit delta codes. If the number of delta codes is not a multiple of five, a five 3-bit multiple must be padded out by making remaining deltas go back upon themselves. Example: The following device data draws a 4 pixel horizontal line at TV line 100 and TV pixel 200 at graphics level 3.

\[02H(1EH) \quad 83H \ C6H \ D0H \ C3H \ C8H \ 40H \ 48H \ 1EH(03H)\]

F. **Routing Code 84H - TV Image Line Load 3-Bit Mode**

Both intensive and transactional modes.

All data is packed as 3-bit codes with five 3-bit codes per two bytes according to the following format:

If the two bytes are made of these bits,

\[
\text{Byte 1} = A_1 A_2 B_1 B_2 B_3 C_1 C_2 C_3 \\
\text{Byte 2} = A_3 A_4 D_1 D_2 D_3 E_1 E_2 E_3
\]

then,
<table>
<thead>
<tr>
<th>A1</th>
<th>A2</th>
<th>A3</th>
<th>A4</th>
<th>3-bit code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>3</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>4</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>5</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>6</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>7</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>Unused</td>
</tr>
</tbody>
</table>

Bit groups B, C, D, and E are themselves the 3-bit codes. The first 3-bit code is a blowup factor to be applied to both lines and pixels. One means no repeat.

The next three 3-bit codes are concatenated to form a 9-bit group (MSB's last) that define the TV starting line number.

All other 3-bit codes are an encoding of 6-bit data along the horizontal line. Image pixel data is assumed to be six bits. The starting pixel on each line is assumed to be zero. Each 3-bit code defines how to derive the next pixel from the last one as follows:

<table>
<thead>
<tr>
<th>CODE</th>
<th>EFFECT</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>Next 6-bit pixel same as last</td>
</tr>
<tr>
<td>1</td>
<td>Next 2 same as last</td>
</tr>
<tr>
<td>2</td>
<td>Next 3 same as last</td>
</tr>
<tr>
<td>3</td>
<td>Last+1</td>
</tr>
<tr>
<td>4</td>
<td>Last-1</td>
</tr>
<tr>
<td>5</td>
<td>Last+2</td>
</tr>
<tr>
<td>6</td>
<td>Last-2</td>
</tr>
<tr>
<td>7</td>
<td>Escape, next two 3-bit codes form new pixel</td>
</tr>
</tbody>
</table>

There are 640 pixels assumed in a horizontal line. If fewer are sent, the remainder are set to 0. If more are sent, they are ignored. (More may be sent to comply with the 3-bit packing rule.)

The worst case for this encoding scheme (constant escapes) will produce 768 bytes of data for one line, actual image data will result in sending a much smaller number of bytes, thus increasing the effective transmission rate of the communication line.

**G. Routing Code 85H - 6-bit Enhancement Table Load**

Both intensive and transactional modes.

The enhancement table is considered to be a 64 cell memory, each cell consisting of 15-bit data values (5 blue, 5 green, 5 red) bit inverted. Each 6-bit video pixel is used as an index into these cells. The three color output of a cell is used to define the color of each pixel. Each byte sent must have one or both of the two MSB's set to 1's. An
enhancement table load message consists of one byte containing the
starting cell number, and one or more sets of three bytes each (1 blue,
1 green, 1 red) defining 15-bit data to be written into the first cell
specified and increasingly higher order cells.

H. Routing Code 86H – Video Control 6-bit Mode

Intensive mode only.

A video control message may be up to 12 bytes long where:

<table>
<thead>
<tr>
<th>BYTE</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 &amp; 2</td>
<td>9-bit cursor center line right justified in 12-bit group</td>
</tr>
<tr>
<td>3 &amp; 4</td>
<td>10-bit cursor center element right justified in 12-bit group</td>
</tr>
<tr>
<td>5 &amp; 6</td>
<td>One-half cursor vertical size right justified as above</td>
</tr>
<tr>
<td>7 &amp; 8</td>
<td>One-half cursor horizontal size right justified as above</td>
</tr>
<tr>
<td>9</td>
<td>Cursor type (1 of 5). Type 1 = Box, Type 2 = Crosshair, Type 3 = Box with crosshair, Type 4 = Solid Rectangle, Type 5 = Star Wars</td>
</tr>
<tr>
<td>10 &amp; 11</td>
<td>Frame number to display, right justified</td>
</tr>
<tr>
<td>12</td>
<td>Dual channel enable control (bit 5 [where LSB is bit 0]) and enable bits FO</td>
</tr>
</tbody>
</table>

Graphics and cursor color

If bit 5 = 0, single channel display
If bit 5 = 1, dual channel display (thru 12 bit colorizor)

Bits 4 thru 8 form cursor color and graphic enable disable where:

00H = no cursor, no graphics
01H = red cursor, graphics
02H = green cursor, graphics
03H = blue cursor, graphics
04H = yellow cursor, graphics
05H = cyan cursor, graphics
06H = magenta cursor, graphics
07H = white cursor, graphics
08H = black cursor, graphics
09H = red cursor, no graphics
0AH = green cursor, no graphics
OBH = blue cursor, no graphics
OCH = yellow cursor, no graphics
ODH = cyan cursor, no graphics
OEH = magenta cursor, no graphics
OFH = white cursor, no graphics
10H = black cursor, no graphics

It is only necessary to send control bytes thru the control to be changed. If anything in the list is to change, every control byte before it must be sent.

I. Routing Code 87H - TV Image Line Load 6-bit Mode

Both intensive and transactional modes.

All data is in 6 bit mode (six LSB's are data bits and two MSB's are set to ones) bytes 1 and 2 are a 9-bit TV line number where:

```
Byte 1 = 1 1 * * Y8 Y7 Y6
Byte 2 = 1 1 Y5 Y4 Y3 Y2 Y1 Y0
```

* = don't care
Y = TV line number to load

Byte 3 to N where N < 642 are 6-bit pixel values along the TV line. If N < 642, the remaining pixels on the TV line are set to 0.

J. Routing Code 88H - TV Frame to Load

Both intensive and transactional mode.

This is a 2 byte message in 6-bit mode where 12 bits (right justified) form a 12 bit TV frame to send data to/from routing codes 84H or 87H.

K. Routing Code 89H - 12-bit Enhancement Table Load

Both intensive and transactional modes.

The enhancement table is an array of 64x64 cells, each cell consisting of 15 bit data values (5 blue, 5 green, 5 red) bit inverted. The table is used to display two different image frames simultaneously. The intensity of a pixel in frame A (six bits) indexes into one dimension of the table, the intensity of the corresponding pixel in frame B indexes into the other dimension. The enhancement cell selected defines the color of the pixel pair.

Row 0 and column 0 are reserved for use with the six bit enhancement tables, consequently the software that loads the enhancement table remaps the table into an array of 63x63 cells. A message that specifies loading element (0,0), for example, is actually loading element (1,1) in the enhancement table memory.
An enhancement table load message consists of one byte containing the starting enhancement row number (6 LSB's), one byte containing the column number (6 LSB's), and one or more sets of three bytes each (1 blue, 1 green, 1 red) defining 15 bit data to be written into the first cell specified and increasingly higher column numbers. The five LSB's in each byte of the three color sets will contain the data. Enhancement data must be loaded one row at a time. Data that is written off the end of a row will alter the six bit enhancement tables. All data in the message must have one or both of the 2 MSB's set to ones.

L. **Routing Code 8AH - ID Request**

Both intensive and transactional.

This is a request for terminal ID and image configuration sent by the host. There is no data in this message (ex. 02H, 8AH, 1EH[03H]).

The terminal will respond with ID, number of image frames, and number of graphic frames.

M. **Routing Code 8BH - Graphics Device (Delta Mode)**

Both intensive and transactional modes.

This routing code sends data to a terminal with multiple frame graphics. This function allows erasing the graphics frame or writing a line segment of 1 or more points.

The graphics frame origin is at the upper left corner of the image space.

1. **Graphics Erase Command**

   The message causes the graphics frame specified to be erased. The message contains only one byte (MSB=1) containing the graphics frame to erase.

   Example: 02H(1EH) 8BH 81H 1EH(03H)

   The above message will cause graphics frame 1 to be erased.

2. **Graphics Line Segment Command**

   Five header bytes are required following the 8BH (routing code):

   Byte 1  1 F6 F5 F4 F3 F2 F1 F0
   Byte 2  1 1 L4 L3 L2 L1 L0 Y8
   Byte 3  1 1 Y7 Y6 Y5 Y4 Y3 Y2
   Byte 4  1 1 Y1 Y0 X9 X8 X7 X6
   Byte 5  1 1 X5 X4 X3 X2 X1 X0
where:
F6-F0 define graphics frame to write
L4-L0 define the graphics value to write (0-1FH)
Y8-Y0 define the TV line to set the pen down (0-499)
X9-X0 define the TV pixel to set the pen down (0-639)

In all cases, *0 is the least significant bit.

If a single point is to be written, no additional bytes are required.
If a line segment is to be drawn, additional data must be packed (using 2 bytes) as five 3-bit values. These 3-bit values define relative delta moves, each in one of the eight cardinal directions.

<table>
<thead>
<tr>
<th>3-bit Data Value</th>
<th>Relative Pen Motion</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>X to X+1 ; Y to Y</td>
</tr>
<tr>
<td>1</td>
<td>X to X-1 ; Y to Y</td>
</tr>
<tr>
<td>2</td>
<td>X to X ; Y to Y+1</td>
</tr>
<tr>
<td>3</td>
<td>X to X ; Y to Y-1</td>
</tr>
<tr>
<td>4</td>
<td>X to X+1 ; Y to Y+1</td>
</tr>
<tr>
<td>5</td>
<td>X to X-1 ; Y to Y+1</td>
</tr>
<tr>
<td>6</td>
<td>X to X+1 ; Y to Y-1</td>
</tr>
<tr>
<td>7</td>
<td>X to X-1 ; Y to Y-1</td>
</tr>
</tbody>
</table>

Furthermore, these 3-bit values must be packed as five data values into two bytes according to the following format:

If the two bytes are made of these bits,

Byte 1 = A1 A2 B1 B2 B3 C1 C2 C3
Byte 2 = A3 A4 D1 D2 D3 E1 E2 E3

then,

<table>
<thead>
<tr>
<th>A1</th>
<th>A2</th>
<th>A3</th>
<th>A4</th>
<th>3-bit Delta Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>3</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>4</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>5</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>6</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>7</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>Unused</td>
</tr>
</tbody>
</table>

Bit groups B, C, D, and E are themselves the 3-bit delta codes. If the number of delta codes is not a multiple of five, a five 3-bit multiple must be padded out by making remaining deltas go back upon themselves. Upon completion of 3-bit data for 1 line segment (padded to a two byte group), a pen lift control character (1DH), followed by all data normally sent following the 8BH routing code, may be sent thus allowing multiple line segments in one message packet.
N. **Routing Code 8CH - McIVAM Pallet Graphics Colors**

Intensive mode only.

This message allows color enhancements of graphics (3 bits) by loading color lookup tables. There are eight color table cells (two bytes each). Each cell is made up of three 5-bit groups (5 blue, 5 green, 5 red). Data sent under this routing code must be in three byte multiples with the five LSB's of each byte set according to the color level intensity for each color and one or both of the two MSB's set = 1. A maximum of eight 3-byte groups may be sent (24 bytes).

O. **Routing code 8DH - McIVAM Eigen Disk Record**

Intensive mode only.

This routing code causes an image currently displayed on the terminal to be recorded onto the desired Eigen disk frame. Data for this device is 256 bytes long in 6-bit mode (2 MSB's = 1). The data is right justified frame number of Eigen disk to record onto.

P. **Routing Code 8EH - Graphics Fill Mode**

Both intensive and transactional modes.

This message causes a specified graphics frame to have a specified number of pixels along a specified horizontal line starting at a specified pixel to be written to a desired graphics level. Five header bytes are required following the 8EH (routing code):

```
Byte 1       1  F6 F5 F4 F3 F2 F1 F0
Byte 2       1  1  L4 L3 L2 L1 L0 Y8
Byte 3       1  1  Y7 Y6 Y5 Y4 Y3 Y2
Byte 4       1  1  Y1 Y0 X9 X8 X7 X6
Byte 4       1  1  X5 X4 X3 X2 X1 X0
```

where

- **F6-F0** define graphics frame to write
- **L4-L0** define the graphics value to write (0-1FH)
- **Y8-Y0** define the TV line to set the pen down (0-499)
- **X9-X0** define the TV pixel to set the pen down (0-639)

In all cases, *0 is the least significant bit.*

Two bytes (in 6-bit mode) containing the right justified number of pixels to set to the desired graphics level must follow. A pen lift control (1DH) may follow if more lines are desired. If so, five header bytes and two pixel count bytes must follow.
Q. **Routing Code 8FH - Graphics End Point Mode**

Both intensive and transactional mode.

This routing code causes graphics at the level specified to be written on the graphics frame desired given end points to line segments.

Five header bytes are required following the 8FH (routing code):

- **Byte 1**: 1 F6 F5 F4 F3 F2 F1 F0
- **Byte 2**: 1 1 L4 L3 L2 L1 L0 Y8
- **Byte 3**: 1 1 Y7 Y6 Y5 Y4 Y3 Y2
- **Byte 4**: 1 1 Y1 Y0 X9 X8 X7 X6
- **Byte 4**: 1 1 X5 X4 X3 X2 X1 X0

where 
- **F6-F0** define graphics frame to write
- **L4-L0** define the graphics value to write (0-1FH)
- **Y8-Y0** define the TV line to set the pen down (0-499)
- **X9-X0** define the TV pixel to set the pen down (0-639)

In all cases, *0 is the least significant bit.

Four bytes specifying point 2 of the end points are required where:

- **Byte 6**: 1 * * * * * Y8 Y7
- **Byte 7**: 1 Y6 Y5 Y4 Y3 Y2 Y1 Y0
- **Byte 8**: 1 * * * * * X9 X8 X7
- **Byte 9**: 1 X6 X5 X4 X3 X2 X1 X0

where 
- *** means don't care**
- **Y8-Y0** define the TV line number of the second end point
- **X9-X0** define the TV pixel number of the second end point

In all cases, *0 is the least significant bit.

If an additional segment connected to the last end point in desired, another four byte set as defined above is required. If the pen is to be lifted, a penup (1DH) must be sent followed by a 9 byte set under the starting format of byte 1 above.

R. **Routing Code 90H - Graphics in both Delta and End Point Mode**

Both intensive and transactional modes.

This message packet allows data to be sent to any graphics frame in both end point and delta modes. This allows the data bits sent to the terminal to be minimized. A pixel repeat function (wide lines) dotted lines, and a pen lift function are also employed.

Eight header bytes are required following the 90H (routing code)

- **Byte 1**: 1 F6 F5 F4 F3 F2 F1 F0
- **Byte 2**: 1 L4 L3 L2 L1 L0 D5 D4
Byte 3 1 D3 D2 D1 D0 G4 G3 G2
Byte 4 1 G1 G0 S5 S4 S3 S2 S1
Byte 5 1 S0 E4 E3 E2 E1 E0 M
Byte 6 1 Y8 Y7 Y6 Y5 Y4 Y3 Y2
Byte 7 1 X1 Y0 X9 X8 X7 X6 X5
Byte 8 1 X4 X3 X2 X1 X0  *  *

where
F6-F0 Define graphics frame to write
L4-L0 Define the graphics value to write (0-1FH)
D5-D0 Define length of dashed line segments in pixels.
If D5-D0=0, solid lines are assumed and G4-G0, S5-S0 are not used.
G4-G0 Define the graphics value to write gaps between dashed line segments, i.e., if G4-G0 = 0, gaps are blank, otherwise gaps are at value G4-G0.
S5-S0 Define length of gap segments in pixels. If S5-S0=0, solid lines are assumed and G4-G0, D5-D0 are not used.
E4-E0 Define pixel repeat factor (0 means 1 pixel wide, 1 means 2 pixels wide, etc.
M Define mode select (0 = delta, 1 = end point)
Y8-Y0 Define the TV line to set the pen down (0-499)
X9-X0 Define the TV pixel to set the pen down (0-639)
* Means don't care

In all cases *0 is the least significant bit. If mode (M) = 1 (End Point Mode), the following data are expected:

Three bytes specifying point 2 of the end points are required where:

byte 9 1 Y8 Y7 Y6 Y5 Y4 Y3 Y2
byte 10 1 Y1 Y0 X9 X8 X7 X6 X5
byte 11 1 X4 X3 X2 X1 X0  *  *

where:  * means don't care

Y8-Y0 define the TV line number of the second end point
X9-X0 define the TV pixel number of the second end point

In all cases, * is the least significant bit. If an additional segment connected to the last end point is desired, another 3 byte set as defined above is required. If mode (M) = 0 (Delta mode), the following data is expected: data must be packed (using 2 bytes) as five 3-bit
values. These 3-bit values define relative delta moves, each in one of the eight cardinal directions.

<table>
<thead>
<tr>
<th>3-bit Data Value</th>
<th>Relative Pen Motion</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>X to X+1; Y to Y</td>
</tr>
<tr>
<td>1</td>
<td>X to X-1; Y to Y</td>
</tr>
<tr>
<td>2</td>
<td>X to X; Y to Y+1</td>
</tr>
<tr>
<td>3</td>
<td>X to X; Y to Y-1</td>
</tr>
<tr>
<td>4</td>
<td>X to X+1; Y to Y+1</td>
</tr>
<tr>
<td>5</td>
<td>X to X-1; Y to Y+1</td>
</tr>
<tr>
<td>6</td>
<td>X to X+1; Y to Y-1</td>
</tr>
<tr>
<td>7</td>
<td>X to X-1; Y to Y-1</td>
</tr>
</tbody>
</table>

Furthermore, these 3-bit values must be packed as 5 data values into 2 bytes according to the following format:

If the 2 bytes are made of these bits,

Byte 1 - A1 A2 B1 B2 B3 C1 C2 C3  
Byte 2 - A3 A4 D1 D2 D3 E1 E2 E3

then,

<table>
<thead>
<tr>
<th>A1</th>
<th>A2</th>
<th>A3</th>
<th>A4</th>
<th>3-bit Delta Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>3</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>4</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>5</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>6</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>7</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>Unused</td>
</tr>
</tbody>
</table>

Bit groups B, C, D, and E are themselves the 3-bit delta codes if the number of delta codes is not a multiple of 5, a five 3-bit multiple must be padded out by making remaining deltas go back upon themselves if the pen is to be lifted, a penup (31H) must be sent followed by a 3 byte set under the starting format of byte 6.

If a mode switch is desired (end point to delta or delta to end point), a mode switch control (30H) must be sent followed by data in the opposite mode format.

S. **Routing Code 91H - TV Image Control**

Transactional mode only.

This message is always 7 bytes long. This message allows the host CPU to define the state of TV images at the terminal. The following defines
the format of the 7 data bytes where "Bx" stands for bit "x", B7 is the MSB, and B0 is the LSB:

Byte 1 B7=1, B6=1, B5-B0 are 6 MSB's for frame to show
Byte 2 B7=1, B6=1, B5-B0 are 6 LSB's for frame to show
Byte 3 B7=1, B6=1, B5-B0 are 6 MSB's of upper bound frame in a movie loop
Byte 4 B7=1, B6=1, B5-B0 are 6 LSB's of upper bound frame in a movie loop
Byte 5 Same as byte 3 but for lower bounds
Byte 6 Same as byte 4 but for lower bounds
Byte 7 B7=1, B6=1, B5-B3=3 bit reserved group,
   B2=Display key
   B1=Movie Key
   B0=Move Enable Key

If display key (B2) = 1, TV screen shows image defined by bytes 1 & 2
If display key (B2) = 0, TV image is not visible (black screen)
If movie key (B1) = 0, no movie looping
If movie key (B1) = 1, movie looping enabled
If move enable key = 0, frame cannot switch regardless of movie key state
If move enable key = 1, frame can be changed

T. Routing Code 92H - Graphic Image Control

Transactional mode only.

This message is always 7 bytes long. This message allows the host CPU to define the state of graphic images at the terminal. The following defines the format of the 7 data bytes where "Bx" stands for bit "x", B7 is the MSB, and B0 is the LSB:

Byte 1 B7=1, B6=1, B5-B0 are 6 MSB's for graphic frame to show
Byte 2 B7=1, B6=1, B5-B0 are 6 LSB's for graphic frame to show
Byte 3 B7=1, B6=1, B5-B0 are 6 MSB's of upper bound graphic frame in a movie loop
Byte 4 B7=1, B6=1, B5-B0 are 6 LSB's of upper bound graphic frame in a movie loop
Byte 5 Same as byte 3 but for lower bounds
Byte 6 Same as byte 4 but for lower bounds
Byte 7 B7=1, B6=1, B5-B3=3 bit reserved group,
   B2=Display key
   B1=Movie Key
   B0=Move Enable Key

If display key (B2) = 1, TV screen shows graphic image defined by bytes 1 & 2
If display key (B2) = 0, graphic image is not visible
If movie key (B1) = 0, no movie looping of graphics
If movie key (B1) = 1, movie looping enabled
If move enable key = 0, graphic frame cannot switch regardless of movie key state
If move enable key = 1, graphic frame can be changed
U. Routing Code 93H - Primary Cursor State

Transactional mode only.

This message allows the host to control the cursor. This message always contains 10 bytes where "BX" stands for bit "x", B7 is the MSB, and B0 is the LSB:

Byte 1  B7=1, B6=1, B5-B0 are 6 MSB's of 1/2 cursor line size
Byte 2  B7=1, B6=1, B5-B0 are 6 LSb's of 1/2 cursor line size
Byte 3  B7=1, B6=1, B5-B0 are 6 MSB's of 1/2 cursor element size
Byte 4  B7=1, B6=1, B5-B0 are 6 LSb's of 1/2 cursor element size
Byte 5  B7=1, B6=1, B5-B0 are 6 MSB's of cursor line position
Byte 6  B7=1, B6=1, B5-B0 are 6 LSb's of cursor line position
Byte 7  B7=1, B6=1, B5-B0 are 6 MSB's of cursor element position
Byte 8  B7=1, B6=1, B5-B0 are 6 LSb's of cursor element position
Byte 9  B7=1, B6-B0 are 7 bit cursor type (only 5 are currently implemented)

where:  Type 1 = Box
        Type 2 = Cross hair
        Type 3 = Box with cross hair
        Type 4 = Solid rectangle
        Type 5 = Star wars

Byte 10 B7=1, B6=1, B5-B3 are cursor color (1 bit each G, G, B),
        B2-B0 are a number implying cursor control
where:
  0  cursor unlinked (size and position according to bytes 1 to 8)
  1  position with vernier (position determined via joysticks
      rather than bytes 5 to 8
  2  size, no position
  3  size and position (size and position determined by joysticks
      rather than bytes 1 to 8)
  4  velocity, no position
  5  velocity (position determined by joysticks according to
      velocity cursor algorithm)
  6  stereo cursor, no joystick control
  7  stereo cursor, joystick control

V. Routing Code 94H - Movie Loop Dwell

Transactional mode only.

This message allows the host to choose dwell rates for frames in a movie loop. A dwell unit is considered to be 1/30 second. A maximum of 12 dwell values may be sent. Dwell value 1 pertains to image 1, dwell 2 to image 2, etc. If there are more images in a movie than dwells, the 12th dwell value is used repeatedly to the end of the movie loop. This message is optional and need be sent only if dwells different from the default are desired. Once dwell values are sent to the terminal, they
will be retained for any subsequent movie loops until a new dwell message is received by the terminal. The form of a dwell message is:

Byte 1 B7=1, B6=B0 define dwell for image 1
Byte 2 B7=1, B6=B0 define dwell for image 2 (optional)
Byte n Same as above where n must be ≤ 12 (optional)

W. Routing Code 95H - Graphics-Cursor Color Pallet

Both intensive and transactional modes.

This message allows changing graphic and/or cursor colors in terminals equipped with the colorizing pallet hardware. The following is a description of the pallet lookup organization.

There are 32 graphic lookup cells (cells 0 to 31).
There are 32 cursor lookup cells (cells 32 to 63).
The total lookup table is 64 cells long (128 bytes).

Cells are addressed during display time by concatenating five graphic bits and 1 cursor bit (the cursor bit addresses the upper half of the lookup table).

The form of a cell is two bytes long where:

Byte 1 G2 G1 G0 B4 B3 B2 B1 B0
Byte 2 VE R4 R3 R2 R1 R0 G4 G3

B4 to B0 Define blue component output.
G4 to G0 Define green component output.
R4 to R0 Define red component output.

VE is video enable (if VE=0 contents of cell is shown, if VE=1 video from image is shown (if VE=1 all bits in the cell must be set to 1's)).

The format of a routing code 95H is as follows:

Bytes must come in 3 byte groups with a minimum of 1 group (for cell 0) and a maximum of 64 groups.

Byte 1 1 1 G2 G1 G0 B4 B3 B2
Byte 2 1 1 R1 R0 G4 G3 B1 B0
Byte 3 1 1 * * VE R4 R3 R2

* are don't cares
G's, B's, R's, and VE are loaded to hardware as indicated above under lookup table description.

Note: The first 3 byte group data is loaded to cell 0, the second 3 byte group to cell 1, etc.
X. **Routing Code 96H - Multi-video Channel Enable-Disable**

Transactional mode only.

This message contains one data byte used to enable dual channel video display thru the 12 bit colorizer lookup table for terminals equipped with that hardware.

If data byte = ClH, dual channel enabled
If data byte = COH, dual channel disabled

Y. **Routing Code 97H - Terminal State Request**

Transactional mode only.

This message contains one byte of data specifying a delay count. Reception of the routing code by the terminal causes the terminal to send a complete state of the terminal to the host via routing codes 91H, 92H, 93H, 96H, 98H, 9AH, and 9BH as defined below under message formats for terminal to host.

Byte 1  B7=1, B6=1, B5-B0 are delay count before state response is sent (1/60 sec)

Z. **Routing Code 99H - Transactional Mode Switch**

Both intensive and transactional modes.

This message has no data following the routing code. If a terminal is in intensive operating mode when this routing code is received, the terminal will switch to transactional mode. If the terminal is in transactional mode, it will delay its response to the host by an additional 1/15 second (until a maximum delay of 2½ sec). This allows less host interruption during idle periods. In transactional mode, the host uses this as an idle and always answers immediately.

Z1. **Routing Code 9CH - Auto Prompt**

This message consists of up to 80 ASCII characters OR'd with 80H. These data will be preceded by a line feed, displayed on the CRT, and stored in the keyboard data input buffer as though the user had keyed them in. The user may then use the keyboard to edit these characters or append to the character string. The host is allowed to send only one such message until the response string (from keyboard data) is received. Line feeds (0AH) and carriage returns (ODH) are not allowed. All other ASCII characters are allowed.
22. Routing Code 9DH – Velocity Increments

Both intensive and transactional modes.

This message allows the host to set the velocity increments for the velocity cursor algorithm. A maximum of 12 increments may be sent. If there are more images than increments, the 12th increment will be used repeatedly to the end of the movie loop. This message is optional and is sent only if increments different than the default are desired. Increments sent to the terminal are used until a new velocity increment message is received by the terminal.

The form of the velocity increment is:

Value 1 for image 1
Value 2 for image 2

...

Value n for image n (n ≤ 12)

Each value is two bytes long in the following form:

Byte 1 B7=1, B6-B0 are 7 MSB's of an increment
Byte 2 B7=1, B0 is LSB of an increment


Transactional mode only.

This message is used to allow cloud tracking cursor control on the terminal. The space bar invokes this function when in Windco mode. The first spacebar causes the terminal to collect state information, change color and size of cursor, and step image frame. Second spacebar sends the selected state information to the host and returns cursor to starting color and size (see 9FH in terminal to host). It also steps the image frame if the frame was at the end of the movie loop. Lag size is change in cursor size from first to second frame in procedure.

Byte 1 B7=1, B6=1, B5 is mode (1 yes, 0 no)
Byte 2 B7=1, B6=1, B5-B3 are first color bits, B2-B0 are second color bits
Byte 3 B7=1, B6=1, B5-B0 are 6 MSB's of 1/2 cursor line lag size
Byte 4 B7=1, B6=1, B5-B0 are 6 LSB's of 1/2 cursor line lag size
Byte 5 B7=1, B6=1, B5-B0 are 6 MSB's of 1/2 cursor element lag size
Byte 6 B7=1, B6=1, B5-B0 are 6 LSB's of 1/2 cursor element lag size
Z4. Routing Code AOH – Stereo Cursor State

Transactional mode only.

This message allows the host to control the cursor when in stereo cursor mode (see routing code 93H). This message always contains 10 bytes where:

Byte 1 B7=1, B6=1, B5-B0 are 6 MSB's of 1/2 cursor line size
Byte 2 B7=1, B6=1, B5-B0 are 6 LSB's of 1/2 cursor line size
Byte 3 B7=1, B6=1, B5-B0 are 6 MSB's of 1/2 cursor element size
Byte 4 B7=1, B6=1, B5-B0 are 6 LSB's of 1/2 cursor element size
Byte 5 B7=1, B6=1, B5-B0 are 6 MSB's of cursor line position
Byte 6 B7=1, B6=1, B5-B0 are 6 LSB's of cursor line position
Byte 7 B7=1, B6=1, B5-B0 are 6 MSB's of cursor element position
Byte 8 B7=1, B6=1, B5-B0 are 6 LSB's of cursor element position
Byte 9 B7=1, B6-B0 are 7 bit cursor type (only 5 are currently implemented)

where: type 1 = box
      type 2 = cross hair
      type 3 = box with cross hair
      type 4 = solid rectangle
      type 5 = star wars

Byte 10 B7=1, B6=1, B5-B0 are cursor color (bit each R,G,B)

VI. ROUTING CODES – TERMINAL TO HOST

In intensive mode, there are only two forms of messages sent from the terminal to host NAK and terminal state. In transactional mode, several routing codes are employed.

A. Return Message – Negative Acknowledge (NAK)

Both intensive and transactional modes.

This message is sent in response to receiving a message with a bad checksum, thus requesting retransmission. The form of the message is:

SYN SYN NAK

B. Terminal State Response

Intensive mode only.

This message has no routing code, however, the data is preceded by SYN, SYN, STX, 10 data bytes (14 if an ID response is pending, 22 if a data tablet is implemented of the terminal), etc.
This message returns the state of all the user input devices to the host. The form of the 10 bytes is:

Byte 1  80H OR'd with an ASCII character from the keyboard
Byte 2  Joystick 1 vertical 6 MSB's OR'd with COH
Byte 3  Joystick 1 vertical 6 LSB's OR'd with COH
Byte 4  Joystick 1 horizontal 6 MSB's OR'd with COH
Byte 5  Joystick 1 horizontal 6 LSB's OR'd with COH
Byte 6-9 Same as joystick 1 but for joystick 2
Byte 10 Status byte of terminal resources as follows

<table>
<thead>
<tr>
<th>Bit</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>7</td>
<td>Set=1</td>
</tr>
<tr>
<td>6-3</td>
<td>Reserved for future expansion</td>
</tr>
<tr>
<td>2</td>
<td>Set for TV load buffers available</td>
</tr>
<tr>
<td>1</td>
<td>Set for line printer buffer available</td>
</tr>
<tr>
<td>0</td>
<td>Set for CRT buffer available</td>
</tr>
</tbody>
</table>

The following additional data bytes will be sent in response to a terminal ID request:

Byte 11  7 bit terminal ID with MSB set = 1
Byte 12  7 MSB's of number of image frames in terminal with MSB set = 1
Byte 13  7 LSB's of number of image frames in terminal with MSB set = 1
Byte 14  7 bits of number of graphics frames in terminal with MSB set = 1

If a data tablet is implemented on the terminal, bytes 11 to 14 as defined above, along with the following will be included: 'EX' stands for bit 'X', B7 is the MSB, B0 is the LSB.

Byte 15  B7=1, B6=1, B5-B0 are 6 MSB's of maximum data tablet Y position
Byte 16  B7=1, B6=1, B5-B0 are 6 LSB's of maximum data tablet Y position
Byte 17  B7=1, B6=1, B5-B0 are 6 MSB's of maximum data tablet X position
Byte 18  B7=1, B6=1, B5-B0 are 6 LSB's of maximum data tablet X position
Byte 19  B7=1, B6 is data tablet pen state, 0 means up, 1 down
         B5-B0 are 6 MSB's of current Y position
Byte 20  B7=1, B6 is data tablet pen proximity state, 1 means pen is near tablet, 0 means pen is not near tablet. (If B6=0 all other data for bytes 19-22 is meaningless.)
         B5-B0 are 6 LSB's of current Y position
Byte 21  B7=1, B6=1, B5-B0 are 6 MSB's of current X position
Byte 22  B7=1, B6=1, B5-B0 are 6 LSB's of current X position
C. **Routing Codes 40H and 41H are Used for Asymmetric One Bit Protocol**

Sees communication modes.

D. **Routing Code 81H - Keyboard Characters**

Transactional mode only.

This routing code allows the terminal to send user keyboard characters to the host. Two forms of user keyins a distinguished, single letter keyins and character strings preceded by a line feed and ended with a carriage return. Whenever a user keyin is complete, the terminal will send a routing code 81H message with that keyin. In addition, routing codes 91H, 92H, 93H, 96H, 98H, 9AH, and 9BH will be sent in the same physical message, thus establishing the terminal state at the time of the user keyin.

The form of routing code 81H message is:

One or more ASCII characters in the user keyin are OR'd with 80H and follow the 81H (routing code).

E. **Routing Code 8AH - ID Response**

Transactional mode only.

This message is a 4 byte data response to the host giving the terminal ID, number of image frames and number of graphics frames the terminal is equipped with.

The form of the data following the 8AH is:

<table>
<thead>
<tr>
<th>Byte 1</th>
<th>7 bit terminal ID with MSB set = 1</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte 2</td>
<td>7 MSB's of number of image frames in terminal with MSB set = 1</td>
</tr>
<tr>
<td>Byte 3</td>
<td>7 LSB's of number of image frames in terminal with MSB set = 1</td>
</tr>
<tr>
<td>Byte 4</td>
<td>7 bits of number of graphics frames in terminal with MSB set = 1</td>
</tr>
</tbody>
</table>

F. **Routing Code 91H - TV Image State**

Transactional mode only.

This message is always 7 bytes long. This message allows the host CPU to determine the state of TV images at the terminal. The following defines the format of the 7 data bytes where 'BX' stands for bit 'X', B7 is the MSB, and B0 is the LSB:

<table>
<thead>
<tr>
<th>Byte 1</th>
<th>B7=1, B6=1, B5–B0 are 6 MSB's for frame showing</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte 2</td>
<td>B7=1, B6=1, B5–B0 are 6 LSB's for frame showing</td>
</tr>
</tbody>
</table>
Byte 3  B7=1, B6=1, B5-B0 are 6 MSB's of upper bound frame in a movie loop
Byte 4  B7=1, B6=1, B5-B0 are 6 LSB's of upper bound frame in a movie loop
Byte 5  Same as byte 3 but for lower bounds
Byte 6  Same as byte 4 but for lower bounds
Byte 7  B7=1, B6=1, B5-B3 = 3 bit reserved group,
        B2 = display key
        B1 = movie key
        B0 = move enable key
If display key (B2) = 1, TV screen shows image defined by bytes 1 and 2.
If display key (b2) = 0, TV image is not visible (black screen).

If movie key (B1) = 0, no movie looping.
If movie key (B1) = 1, movie looping enabled.

If move enable key = 0, frame cannot switch regardless of movie key state.
If move enable key = 1, frame can be changed.

G. Routing Code 92H - Graphic Image State

Transactional mode only.

This message is always 7 bytes long. This message allows the host CPU to determine the state of graphic images at the terminal. The following defines the format of the 7 data bytes where 'BX' stands for bit 'X', B7 is the MSB, and B0 is the LSB:

Byte 1  B7=1, B6=1, B5-B0 are 6 MSB's for graphic frame showing
Byte 2  B7=1, B6=1, B5-B0 are 6 LSB's for graphic frame showing
Byte 3  B7=1, B6=1, B5-B0 are 6 MSB's of upper bound graphic frame in a movie loop
Byte 4  B7=1, B6=1, B5-B0 are 6 LSB's of upper bound graphic frame in a movie loop
Byte 5  Same as byte 3 but for lower bounds
Byte 6  Same as byte 4 but for lower bounds
Byte 7  B7=1, B6=1, B5-B3 = 3 bit reserved group,
        B2 = display key
        B1 = movie key
        B0 = move enable key
If display key (B2) = 1, TV screen shows graphic image defined by bytes 1 and 2.
If display key (b2) = 0, graphic image is not visible (black screen).

If movie key (B1) = 0, no movie looping of graphics
If movie key (B1) = 1, movie looping enabled.

If move enable key = 0, graphic frame cannot switch regardless of movie key state.
If move enable key = 1, graphic frame can be changed.
H. Routing Code 93H – Primary Cursor State

Transactional mode only.

This message allows the host to determine the state of the cursor. This message always contains 10 bytes where "BX" stands for bit "X", B7 is the MSB, and B0 is the LSB:

Byte 1 B7=1, B6=1, B5–B0 are 6 MSB's of 1/2 cursor line size
Byte 2 B7=1, B6=1, B5–B0 are 6 LSB's of 1/2 cursor line size
Byte 3 B7=1, B6=1, B5–B0 are 6 MSB's of 1/2 cursor element size
Byte 4 B7=1, B6=1, B5–B0 are 6 LSB's of 1/2 cursor element size
Byte 5 B7=1, B6=1, B5–B0 are 6 MSB's of cursor line position
Byte 6 B7=1, B6=1, B5–B0 are 6 LSB's of cursor line position
Byte 7 B7=1, B6=1, B5–B0 are 6 MSB's of cursor element position
Byte 8 B7=1, B6=1, B5–B0 are 6 LSB's of cursor element position
Byte 9 B7=1, B6–B0 are 7 bit cursor type (only 5 are currently implemented)

where: type 1 = box
type 2 = cross hair
type 3 = box with cross hair
type 4 = solid rectangle
type 5 = star wars

Byte 10 B7=1, B6=1, B5–B3 are cursor color (bit each R,G,B)
B2–B0 are a number implying cursor control

where:
0 cursor unlinked (size and position according to bytes 1 to 8)
1 position with vernier (position determined via joysticks rather than bytes 5 to 8)
2 size, no position
3 size and position (size and position determined by joysticks rather than bytes 1 to 8)
4 velocity, no position
5 velocity (position determined by joysticks according to velocity cursor algorithm)
6 stereo cursor, no joystick control
7 stereo cursor, joystick control

I. Routing Code 96H – Multi-video Channel State

Transactional mode only.

This message contains one data byte used to inform the host of the state of the dual channel video display for terminals equipped with that hardware.

If data byte = C1H, dual channel enabled
If data byte = COH, dual channel disabled
J. Routing Code 98H - Buffer Available Status

Transactional mode only.

This message allows the terminal to inform the host if buffers are available for data to be sent to CRT or line printer. This message is included in every physical message sent to the host while in transactional mode. This message is two bytes long:

- **Byte 1** CRT buffer status (COH = No buffers, CIH = buffers available)
- **Byte 2** Line printer status (COH = No buffers, CIH = buffers available)

This packet is the idle message.

K. Routing Code 99H - Idle Traffic

Transactional mode only.

This message is sent to keep communications open whenever there is no information to exchange. This message has no data following the routing code.

This packet is currently unused.

L. Routing Code 9AH - Raw Joystick Data

Transactional mode only.

This message sends the host the raw joystick data at the terminal. This message is 8 bytes long where:

- **Byte 1** Joystick 1 vertical 6 MSB's OR'd with COH
- **Byte 2** Joystick 1 vertical 6 LSB's OR'd with COH
- **Byte 3** Joystick 1 horizontal 6 MSB's OR'd with COH
- **Byte 4** Joystick 1 horizontal 6 LSB's OR'd with COH
- **Byte 5-8** Same as joystick 1 but for joystick 2

M. Routing Code 9BH - Data Tablet Data

Transactional mode only.

The following data tablet data are sent if the terminal is implemented with a data tablet. 'BX' stands for 'X', B7 is the MSB, B0 is the LSB.

- **Byte 1** B7=1, B6 = Data tablet pen state, 0 means up, 1 means down
- B5-B0 are 6 MSB's of current Y position
Byte 2  B7=1, B6 = Data tablet pen proximity state, 1 means pen is near tablet, 0 means pen is not near tablet. (If B6=0, all other data for bytes 1-4 is meaningless.)
B5-B0 are 6 LSB's of current Y position
Byte 3  B7=1, B6=1, B5-B0 are 6 MSB's of current X position
Byte 4  B7=1, B6=1, B5-B0 are 6 LSB's of current X position

N. **Routing Code 9EH - Data Tablet Pen State**

Transactional mode only.

This message contains no data bytes. It is sent if the pen from the graphics data tablet goes from up to down.

O. **Routing Code 9FH - Windco Response**

Transactional mode only.

This message is in response to the second spacebar if in Windco mode (see 9FH host to terminal).

Byte 1  B7=1, B6=1, B5-B0 are 6 MSB's of first image frame
Byte 2  B7=1, B6=1, B5-B0 are 6 LSB's of first image frame
Byte 3  B7=1, B6=1, B5-B0 are 6 MSB's of second image frame
Byte 4  B7=1, B6=1, B5-B0 are 6 LSB's of second image frame
Byte 5  B7=1, B6=1, B5-B0 are 6 MSB's of first 1/2 cursor line size
Byte 6  B7=1, B6=1, B5-B0 are 6 LSB's of first 1/2 cursor line size
Byte 7  B7=1, B6=1, B5-B0 are 6 MSB's of second 1/2 cursor line size
Byte 8  B7=1, B6=1, B5-B0 are 6 LSB's of second 1/2 cursor line size
Byte 9  B7=1, B6=1, B5-B0 are 6 MSB's of first 1/2 cursor element size
Byte 10 B7=1, B6=1, B5-B0 are 6 LSB's of first 1/2 cursor element size
Byte 11 B7=1, B6=1, B5-B0 are 6 MSB's of second 1/2 cursor element size
Byte 12 B7=1, B6=1, B5-B0 are 6 LSB's of second 1/2 cursor element size
Byte 13 B7=1, B6=1, B5-B0 are 6 MSB's of first cursor line position
Byte 14 B7=1, B6=1, B5-B0 are 6 LSB's of first cursor line position
Byte 15 B7=1, B6=1, B5-B0 are 6 MSB's of second cursor line position
Byte 16 B7=1, B6=1, B5-B0 are 6 LSB's of second cursor line position
Byte 17 B7=1, B6=1, B5-B0 are 6 MSB's of first cursor element position
Byte 18 B7=1, B6=1, B5-B0 are 6 LSB's of first cursor element position
Byte 19 B7=1, B6=1, B5-B0 are 6 MSB's of second cursor element position
Byte 20 B7=1, B6=1, B5-B0 are 6 LSB's of second cursor element position
Byte 21 B7=1, B6=1, B5-B0 are 6 MSB's of first graphic frame
Byte 22 B7=1, B6=1, B5-B0 are 6 LSB's of first graphic frame
Byte 23 B7=1, B6=1, B5-B0 are 6 MSB's of second graphic frame
Byte 24 B7=1, B6=1, B5-B0 are 6 LSB's of second graphic frame

P. **Routing Code AOH - Stereo Cursor State**

Transactional mode only.

This message allows the host to determine the state of the stereo cursor when in stereo cursor mode (see routing code 93H). This message always contains 10 bytes where:

Byte 1  B7=1, B6=1, B5-B0 are 6 MSB's of 1/2 cursor line size
Byte 2  B7=1, B6=1, B5-B0 are 6 LSB's of 1/2 cursor line size
Byte 3  B7=1, B6=1, B5-B0 are 6 MSB's of 1/2 cursor element size
Byte 4  B7=1, B6=1, B5-B0 are 6 LSB's of 1/2 cursor element size
Byte 5  B7=1, B6=1, B5-B0 are 6 MSB's of cursor line position
Byte 6  B7=1, B6=1, B5-B0 are 6 LSB's of cursor line position
Byte 7  B7=1, B6=1, B5-B0 are 6 MSB's of cursor element position
Byte 8  B7=1, B6=1, B5-B0 are 6 LSB's of cursor element position
Byte 9  B7=1, B6-B0 are 7 bit cursor type (only 5 are currently implemented)

where:  type 1 = box
        type 2 = cross hair
        type 3 = box with cross hair
        type 4 = solid rectangle
        type 5 = star wars

Byte 10 B7=1, B6=1, B5-B3 are cursor color (bit each R,G,B)
### APPENDIX A  ROUTING CODE SUMMARY

<table>
<thead>
<tr>
<th>Code</th>
<th>Host to Terminal</th>
<th>Terminal to Host</th>
</tr>
</thead>
<tbody>
<tr>
<td>40H–80H</td>
<td>Reserved</td>
<td>Reserved</td>
</tr>
<tr>
<td>81H</td>
<td>CRT data</td>
<td>Keyboard characters</td>
</tr>
<tr>
<td>82H</td>
<td>Terminal printer</td>
<td>-</td>
</tr>
<tr>
<td>83H</td>
<td>Single graphics device (delta mode)</td>
<td>-</td>
</tr>
<tr>
<td>84H</td>
<td>TV image line load (3-bit mode)</td>
<td>-</td>
</tr>
<tr>
<td>85H</td>
<td>6-bit enhancement table load</td>
<td>-</td>
</tr>
<tr>
<td>86H</td>
<td>Video control (6-bit mode)</td>
<td>-</td>
</tr>
<tr>
<td>87H</td>
<td>TV image line load (6-bit mode)</td>
<td>-</td>
</tr>
<tr>
<td>88H</td>
<td>TV frame to load</td>
<td>-</td>
</tr>
<tr>
<td>89H</td>
<td>12-bit enhancement table load</td>
<td>-</td>
</tr>
<tr>
<td>8AH</td>
<td>ID request</td>
<td>ID response</td>
</tr>
<tr>
<td>8BH</td>
<td>Graphics device (delta mode)</td>
<td>-</td>
</tr>
<tr>
<td>8CH</td>
<td>MCIVAM pallet graphics colors</td>
<td>-</td>
</tr>
<tr>
<td>8DH</td>
<td>MCIVAM eigen disk record</td>
<td>-</td>
</tr>
<tr>
<td>8EH</td>
<td>Graphics fill mode</td>
<td>-</td>
</tr>
<tr>
<td>8FH</td>
<td>Graphics end point mode</td>
<td>-</td>
</tr>
<tr>
<td>90H</td>
<td>Graphics in both delta and end point mode (wide line)</td>
<td>-</td>
</tr>
<tr>
<td>91H</td>
<td>TV image control</td>
<td>TV image state</td>
</tr>
<tr>
<td>92H</td>
<td>Graphic image control</td>
<td>Graphic image state</td>
</tr>
<tr>
<td>93H</td>
<td>Primary cursor state</td>
<td>Primary cursor state</td>
</tr>
<tr>
<td>94H</td>
<td>Movie loop dwells</td>
<td>-</td>
</tr>
<tr>
<td>95H</td>
<td>Graphics-cursor color pallet</td>
<td>-</td>
</tr>
<tr>
<td>96H</td>
<td>Multi-video channel enable</td>
<td>Multi-video channel state</td>
</tr>
<tr>
<td>Code</td>
<td>Description</td>
<td>Code</td>
</tr>
<tr>
<td>------</td>
<td>------------------------------------------</td>
<td>------</td>
</tr>
<tr>
<td>97H</td>
<td>Terminal state request</td>
<td>-</td>
</tr>
<tr>
<td>98H</td>
<td></td>
<td>98H</td>
</tr>
<tr>
<td>99H</td>
<td>Transactional mode switch</td>
<td>99H</td>
</tr>
<tr>
<td>9AH</td>
<td></td>
<td>9BH</td>
</tr>
<tr>
<td>9CH</td>
<td>Auto prompt</td>
<td>9CH</td>
</tr>
<tr>
<td>9DH</td>
<td>Velocity increments</td>
<td>9DH</td>
</tr>
<tr>
<td>9EH</td>
<td></td>
<td>9EH</td>
</tr>
<tr>
<td>9FH</td>
<td>Windco mode state</td>
<td>9FH</td>
</tr>
<tr>
<td>A0H</td>
<td>Stereo cursor state</td>
<td>A0H</td>
</tr>
</tbody>
</table>
INDEX

Note: (nnH) is routing code
      T-H = terminal to host
      H-T = host to terminal

<table>
<thead>
<tr>
<th>SUBJECT</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>Auto prompt (9CH) (H-T)</td>
<td>19</td>
</tr>
<tr>
<td>Buffer available status (98H) (T-H)</td>
<td>26</td>
</tr>
<tr>
<td>Communication modes</td>
<td>2</td>
</tr>
<tr>
<td>Control character definitions</td>
<td></td>
</tr>
<tr>
<td>General</td>
<td>1</td>
</tr>
<tr>
<td>Exceptions</td>
<td>2</td>
</tr>
<tr>
<td>CRT data (81H) (H-T)</td>
<td>4</td>
</tr>
<tr>
<td>Cursor</td>
<td></td>
</tr>
<tr>
<td>Primary cursor state (93H) (H-T)</td>
<td>17</td>
</tr>
<tr>
<td>Second cursor state (A0H) (H-T)</td>
<td>21</td>
</tr>
<tr>
<td>Primary cursor state (93H) (T-H)</td>
<td>25</td>
</tr>
<tr>
<td>Secondary cursor (A0H) (T-H)</td>
<td>28</td>
</tr>
<tr>
<td>Data tablet</td>
<td></td>
</tr>
<tr>
<td>Data (9BH) (T-H)</td>
<td>26</td>
</tr>
<tr>
<td>Pen state (9EH) (T-H)</td>
<td>27</td>
</tr>
<tr>
<td>Delta mode</td>
<td>see graphics</td>
</tr>
<tr>
<td>Dwells, loop</td>
<td>see movie loop dwells</td>
</tr>
<tr>
<td>Eigen disk</td>
<td>see MCIIVAM</td>
</tr>
<tr>
<td>End point mode</td>
<td>see graphics</td>
</tr>
<tr>
<td>Enhancement table load</td>
<td></td>
</tr>
<tr>
<td>6-bit mode (85H) (H-T)</td>
<td>7</td>
</tr>
<tr>
<td>12-bit table (89H) (H-T)</td>
<td>9</td>
</tr>
<tr>
<td>Erase graphics</td>
<td>see graphics</td>
</tr>
<tr>
<td>Fill mode</td>
<td>see graphics</td>
</tr>
<tr>
<td>Frame to load</td>
<td></td>
</tr>
<tr>
<td>TV frame to load (88H) (H-T)</td>
<td>9</td>
</tr>
</tbody>
</table>
Graphics
Single device (83H) (H-T) ....................... 5
Erase (83H) (H-T) ................................ 5
Delta mode (88H) (H-T) .......................... 10
Line segment command (8BH) (H-T) ............. 10
Graphics erase (8BH) (H-T) ...................... 10
Fill mode (8EH) (H-T) ............................ 12
End point mode (8FH) (H-T) ..................... 13
Wide line graphics (90H) (H-T) ................ 13
Graphics image state (92H) (T-H) .............. 24
Graphics cursor pallet (95H) (H-T) ............. 18
Graphics tablet .................................. see data tablet
ID request (8AH) (H-T) ........................... 10
ID response (8AH) (H-T) .......................... 23
Idle traffic (99H) (T-H) .......................... 26
Image control
TV (91H) (H-T) ....................................... 15
Graphic image control (92H) (H-T) ............... 16
Image load ........................................ see TV load
Image state, graphic ............................. see graphics
Image state, TV .................................... see TV image state
Increments, velocity .............................. see velocity increments
Keyboard characters (81H) (H-T) ................... 23
Loop dwells ................................. see movie loop dwells
MCIVAM
Graphics pallet (8CH) (H-T) ...................... 12
Eigen disk record (8DH) (H-T) ................. 12
Message form .................................. 4
Mode state, Windco .............................. see Windco
Mode switch ...................................... see transactional mode switch
Movie loop dwells (94H) (H-T) .................... 17
Multi channel video state (96H) (H-T) ........... 19
Multi channel video state (96H) (T-H) ...................... 25
NACK ................................................................. see negative acknowledge
Negative acknowledge (H-T) ................................. 21
Pallet ................................................................. see MCIVAM, graphics cursor pallet
Printer (82H) (H-T) ................................................. 4
Raw joystick data (9AH) (T-H) ................................. 26
Second cursor ......................................................... see cursor
State request ......................................................... see terminal state request
State response, terminal .......................................... see terminal state response
Stereo cursor state ................................................ see cursor
Terminal printer ..................................................... see printer
Terminal state request (97H) (H-T) ......................... 19
Terminal state response (H-T) ................................. 21
Transactional mode switch (99H) (H-T) ...................... 19
TV image state (91H) (H-T) ...................................... 23
TV load
  3-bit mode (84H) (H-T) ........................................ 6
  6-bit mode (87H) (H-T) ......................................... 9
Velocity increments (9DH) (H-T) .............................. 20
Video control
  6-bit mode (86H) (H-T) ........................................ 8
Windco
  Mode state (9FH) (H-T) ....................................... 20
  Response (9FH) (T-H) ......................................... 27